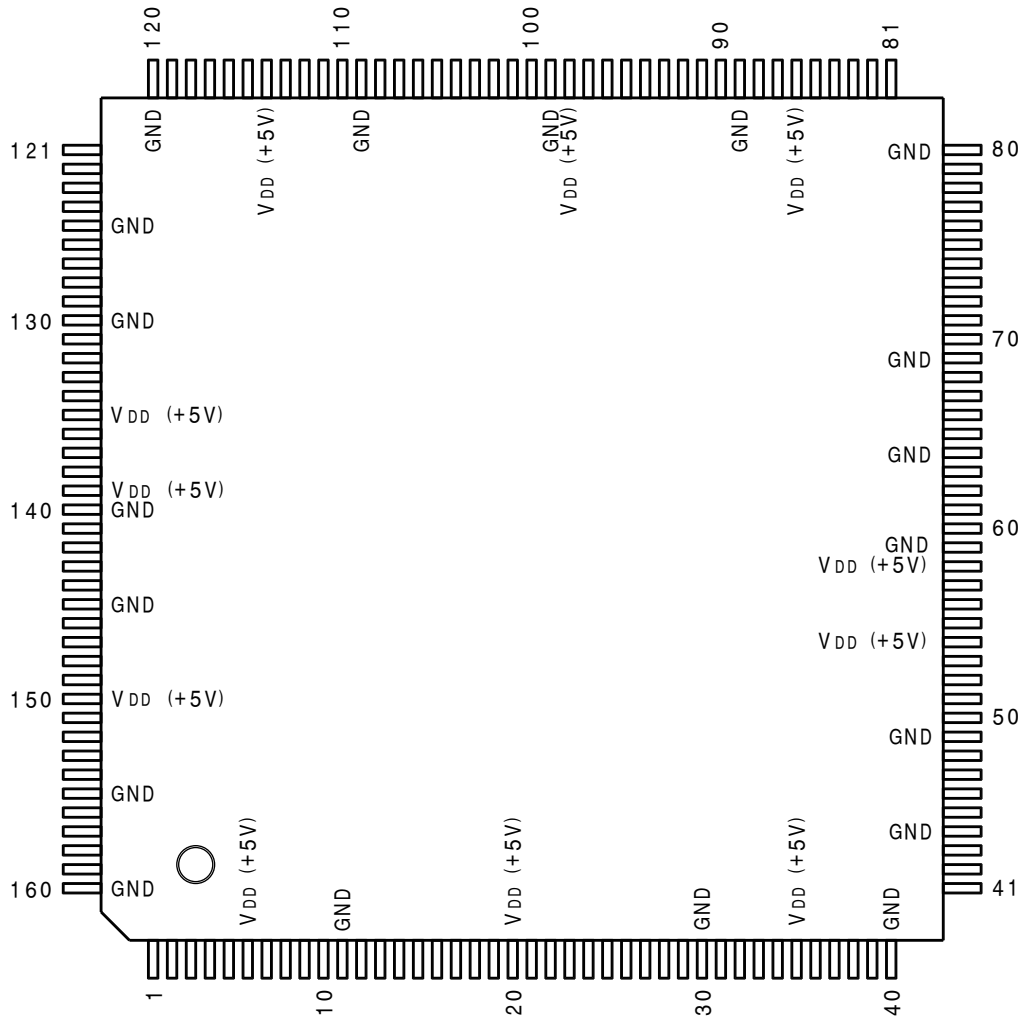


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### C-MOS FIELD-PROGRAMMABLE GATE ARRAY -TOP VIEW-



(V<sub>DD</sub> = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I/O	I01	41	I/O	I035	81	I/O	I064	121	I/O	I098
2	I/O	DCLK/I02	42	I/O	I036	82	I/O	I065	122	I/O	I099
3	I/O	I03	43	I/O	I037	83	I/O	I066	123	I/O	I0100
4	I/O	I04	44	-	GND	84	I/O	I067	124	I/O	I0101
5	I/O	I05	45	I/O	I038	85	I/O	I068	125	-	GND
6	-	VDD	46	I/O	I039	86	-	VDD	126	I/O	I0102
7	I/O	I06	47	I/O	I040	87	I/O	I069	127	I/O	I0103
8	I/O	I07	48	I/O	I041	88	I/O	I070	128	I/O	I0104
9	I/O	I08	49	-	GND	89	-	GND	129	I/O	I0105
10	I/O	I09	50	I/O	I042	90	I/O	I071	130	-	GND
11	-	GND	51	I/O	I043	91	I/O	I072	131	I/O	I0106
12	I/O	I010	52	I/O	I044	92	I/O	I073	132	I/O	I0107
13	I/O	I011	53	I/O	I045	93	I/O	I074	133	I/O	I0108
14	I/O	I012	54	-	VDD	94	I/O	I075	134	I/O	I0109
15	I/O	I013	55	I/O	I046	95	I/O	I076	135	-	VDD
16	I/O	PRB/I014	56	I/O	I047	96	I/O	I077	136	I/O	I0110
17	I/O	I015	57	-	Vsv	97	I/O	I078	137	I/O	I0111
18	I/O	CLKB/I016	58	-	VDD	98	-	VDD	138	-	Vsv
19	I/O	I017	59	-	GND	99	-	GND	139	-	VDD
20	-	VDD	60	-	VPP	100	I/O	I079	140	-	GND
21	I/O	CLKA/I018	61	-	Vks	101	I/O	I080	141	I/O	I0112
22	I/O	I019	62	I/O	I048	102	I/O	I081	142	I/O	I0113
23	I/O	PRA/I020	63	I/O	I049	103	I/O	I082	143	I/O	I0114
24	I/O	I021	64	-	GND	104	I/O	I083	144	I/O	I0115
25	I/O	I022	65	I/O	I050	105	I/O	I084	145	-	GND
26	I/O	I023	66	I/O	I051	106	I/O	I085	146	I/O	I0116
27	I/O	I024	67	I/O	I052	107	I/O	I086	147	I/O	I0117
28	I/O	I025	68	I/O	I053	108	I/O	I087	148	I/O	I0118
29	I/O	I026	69	-	GND	109	-	GND	149	I/O	I0119
30	-	GND	70	I/O	I054	110	I/O	I088	150	-	VDD
31	I/O	I027	71	I/O	I055	111	I/O	I089	151	I/O	I0120
32	I/O	I028	72	I/O	I056	112	I/O	I090	152	I/O	I0121
33	I/O	I029	73	I/O	I057	113	I/O	I091	153	I/O	I0122
34	I/O	I030	74	I/O	I058	114	-	VDD	154	I/O	I0123
35	-	VDD	75	I/O	I059	115	I/O	I092	155	-	GND
36	I/O	I031	76	I/O	I060	116	I/O	I093	156	I/O	I0124
37	I/O	I032	77	I/O	I061	117	I/O	I094	157	I/O	I0125
38	I/O	SDI/I033	78	I/O	I062	118	I/O	I095	158	I/O	I0126
39	I/O	I034	79	I/O	I063	119	I/O	I096	159	I	MODE
40	-	GND	80	-	GND	120	I/O	I097	160	-	GND

CLKA, CLKB ;CLOCK INPUTS  
 DCLK ;DIAGNOSTIC CLOCK INPUT (MULTIFUNCTION PIN)  
 IO1-IO126 ;STANDARD I/Os (IO2, IO14, IO20, IO33;MULTIFUNCTION PINS)  
 MODE ;MULTIFUNCTION PINS CONTROL INPUT  
 PRA, PRB ;DIAGNOSTIC PROBE OUTPUTS (MULTIFUNCTION PINS)  
 SDI ;SERIAL DATA INPUT FOR DIAGNOSTIC PROBE (MULTIFUNCTION PIN)

MULTIFUNCTION PINS CONTROL

MODE	MULTIFUNCTION PIN NO.				
	2	16	23	38	
0	IO2	IO14	IO20	IO33	
1	DCLK	PRA	PRB	SDI	

0;LOW LEVEL

1;HIGH LEVEL

V<sub>PP</sub>, V<sub>SV</sub> ;PROGRAMMING VOLTAGE INPUT.

THESE PINS MUST BE CONNECTED TO V<sub>DD</sub> LEVEL EXCEPT DURING PROGRAMMING.

V<sub>KS</sub> ;PROGRAMMING VOLTAGE INPUT.

THIS PIN MUST BE CONNECTED TO GND LEVEL EXCEPT DURING PROGRAMMING.